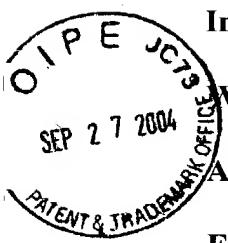


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



In re U.S. Patent Application of )  
WATANABE et al. ) Art Unit 2112  
Application Number: 09/870,630 ) Examiner Myers,  
Filed: June 1, 2001 ) Paul R.  
For: SIGNAL PROCESSING CIRCUIT )  
Attorney Docket No. NITT.0017 )

**RECEIVED**

SEP 30 2004

Technology Center 2100

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

**CERTIFICATION**

This Information Disclosure Statement is submitted after three months from the filing date of the above-identified U.S. patent application and after the mailing date of the first Office Action on the merits of the above-identified application, but prior to issuance of the earlier of any Final Action or Notice of Allowance sent in such application. The fee required under 37 C.F.R. 1.97(c) is submitted herewith.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

Stanley P. Fisher  
Registration Number 24,344

Juan Carlos A. Marquez  
Registration No. 34,072

**REED SMITH LLP**  
3110 Fairview Park Drive  
Suite 1400  
Falls Church, Virginia 22042  
(703) 641-4200  
**September 27, 2004**

Form PTO 1449	ATTY. DOCKET NUMBER NITT.0017	SERIAL NUMBER 09/870,630
U.S. Department of Commerce Patent and Trademark Office	APPLICANT Watanabe et al.	RECEIVED SEP 30 2004
Information Disclosure Statement by Applicant	FILING DATE June 1, 2001	GROUP

## U.S. Patent Documents

Technology Center 2100

## Foreign Patent Documents

**Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)**

		International Search Report dated March 16, 1999
		Yong Yao, "Chromatic's Mpact 2 Boosts 3D", Microprocesor Report, Vol. 10, No. 15, November 18, 1996, 6 pages
		Peter N. Glaskowsky, "Fujitsu Aims Media Processor at DVD", Microprocesor Report, Vol. 10, No. 15, November 18, 1996, pp. 11-13
		Brian Case, "First Trimedia Chip Boards PCI Bus", Microprocesor Report, November 13, 1995, pp. 22-25
		Yong Yao, "Samsung Launches Media Processor", Microprocesor Report, Vol. 10, No. 11, August 26, 1996, 5 pages
		Linley Gwennap, "Mitsubishi Designs DVD Decoder", Microprocesor Report, Vol. 10, No. 16, December 9, 1996, 5 pages
		PCT/JP98/05449, International Preliminary Examination Report dated February 12, 2001
EXAMINER	DATE CONSIDERED	

*EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant*